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12/28/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10035606	FILING DATE 12/28/2001	CLASS 257	SUBCLASS 322	GAU 2811	EXAMINER <i>Hu</i>
**APPLICANTS: Gallia James; Krishnan Srikanth; Krishnan Anand;					
**CONTINUING DATA VERIFIED: <i>N/A</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>N/A</i>					
TITLE : Method and system for reducing charge damage in silicon-on-insulator technology					

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PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	TI-32942 (032350.B365)
Verified and Acknowledged Examiners's initials		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner Application Examiner	
<input type="checkbox"/> PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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